



PATENT

2822
#51A
1800
1010-12

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

WILLIAM D. ROCHE
DAVID D. WU
MASSUD AMINPUR
SCOTT D. LUNING

Examiner: K. Duong

Group Art Unit: 2822

Serial No.: 09/676,197

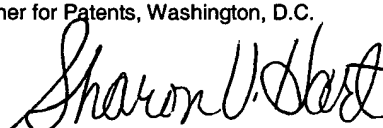
Att'y Docket: 2000.032200/TT3634

Filed: September 28, 2000

For: METHOD OF FORMING A GATE
ELECTRODE ON A SEMICONDUCTOR
DEVICE AND A DEVICE
INCORPORATING SAME

RECEIVED
OCT - 9 2002
TECHNOLOGY CENTER 2800

RESPONSE TO OFFICE ACTION DATED JULY 31, 2002

CERTIFICATE OF MAILING 37 C.F.R. 1.8	
I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date below:	
September 30, 2002	
Date	Signature

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

This paper is submitted in response to the Office Action dated July 31, 2002, for which the three-month date for response is October 31, 2002.

It is believed that no fee is required in connection with this response. However, the Assistant Commissioner is authorized to deduct any fees required under 37 C.F.R. §§ 1.16 to 1.21 from the Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT3634.

Reconsideration of the application is respectfully requested.

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